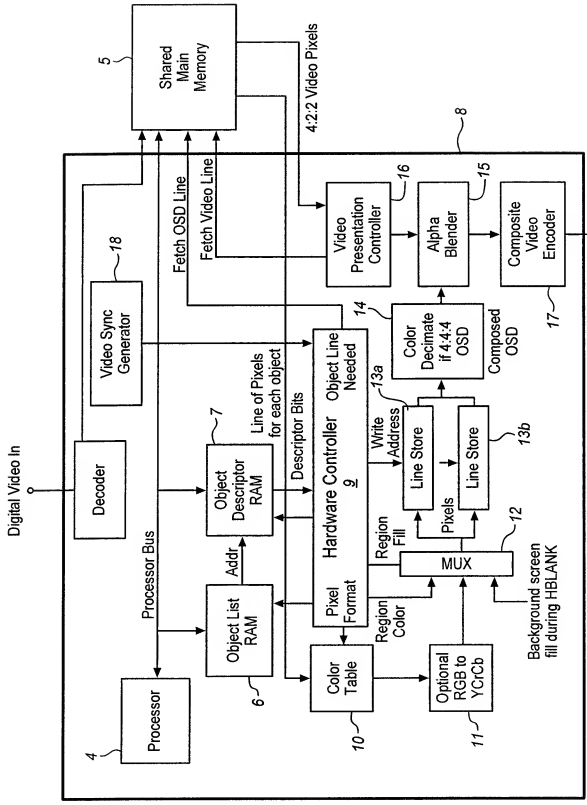
1
Digital Video In
9
Decoder
Processor Bus
4
Processor
Object List RAM
6
Addr
Object Descriptor RAM
7
Line of Pixels for each object
Descriptor Bits
Hardware Controller 9
Pixel Format
Color Table
10
Region Color
Optional RGB to YCrCb
11
Region Fill
MUX
12
Background screen fill during HBLANK
Write Address
13a
Line Store
Pixels
Line Store
13b
Object Line Needed
14
Color Decimate if 4:4:4 OSD
Composed OSD
Video Sync Generator
18
Fetch OSD Line
Fetch Video Line
Shared Main Memory
5
4:2:2 Video Pixels
8
Video Presentation Controller
16
Alpha Blender
15
Composite Video Encoder
17
To Screen

FIG. 1